EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S17	47	(controller WITH (shar$3) WITH resource WITH (reduc$3 OR advantage))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/01 19:19
S24	45	@pd>"20060309" AND 348/207.1. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2007/02/02 17:51
S29	224	(shar$3 ADJ2 resource) WITH controller WITH (processor OR microprocessor)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/01 19:21
S30	3	(shar$3 ADJ2 resource) WITH controller WITH (processor OR microprocessor) WITH (monitor OR display)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/01 19:21
S31	17015	digital NEAR3 (frame OR album)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 12:10
S32	16114	(digital NEAR3 (frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 12:11
S33	4398	(digital ADJ2 (frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 12:11
S34	1307	(digital ADJ2 (frame)) AND (microprocessor OR processor) AND (controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 12:15
S35	29	(digital ADJ2 (frame)) AND (microprocessor OR processor) AND (controller) AND (memory ADJ stick)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 13:01

EAST Search History

S36	645	(digital ADJ2 (frame)) AND (microprocessor OR processor) AND (controller) AND (re$size OR resolution)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 13:01
S37	44	(digital ADJ2 (frame)) AND (microprocessor OR processor) AND (controller) AND ((chang$3 OR modify$3) NEAR3 resolution)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 13:10
S38	298	(digital ADJ2 (frame)) AND (microprocessor OR processor) AND (controller) AND (memory WITH (card OR stick))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 13:10
S39	138	(digital ADJ2 (frame)) AND (microprocessor OR processor) AND (controller) AND (memory WITH (card OR stick)) AND resolution	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 13:10
S40	26	((digital ADJ2 frame) WITH (picture OR photo)) AND (microprocessor OR processor) AND (controller) AND (memory WITH (card OR stick)) AND resolution	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 13:11
S41	5	@pd>"20060807" AND 715/718. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2007/02/02 17:51
S42	34	@pd>"20060807" AND 715/730. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2007/02/02 17:51
S43	6	@pd>"20060807" AND 715/961. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2007/02/02 17:51
S44	2	@pd>"20060807" AND 715/965. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2007/02/02 17:51
S45	57	@pd>"20060807" AND 348/207.1. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2007/02/02 17:52

S46	49	@pd>"20060807" AND 348/231.99. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2007/02/02 17:52
S47	161	@pd>"20060807" AND 361/681. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2007/02/02 17:52
S48	9	@pd>"20060807" AND 361/682. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2007/02/02 17:52
S49	5	(S41 OR S42 OR S43 OR S44 OR S45 OR S46 OR S47 OR S48) AND (digital NEAR2 frame)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	ON	2007/02/02 17:53